

L Number	Hits	Search Text	DB	Time stamp
-	6	yield same virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:44
-	12	lock same virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:28
-	4	relinquish same virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:46
-	3	cooperat\$ same virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:48
-	2	deadlock same virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:49
-	1	hypervisor and virtual adj machine and "spin-lock"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:50
-	5	virtual adj machine and "spin-lock"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:50
-	3	hypervisor and virtual adj machine and "target processor"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:53
-	27	virtual adj machine same (target adj processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:54
-	9	virtual adj machine same (target adj processor) not (compile compiling compiled)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:54
-	60	(718.clas.) and virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:57
-	8	(718/106.ccls.) and virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 14:19
-	16	(718/107.ccls. 718/108.ccls.) and virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 14:24
-	2	5325526.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 14:56

-	5	lpar and schedul\$ and yield	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/08 14:27
-	5	virtual adj machine and request\$ adj yield	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/08 14:33
-	15	virtual adj machine and cooperat\$ adj2 schedul\$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/08 14:40
-	10	virtual adj processor and yield and (reassign repartition dispatch reallocate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/08 14:51
-	9	virtual adj processor and predicated	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/08 14:54
-	18	virtual adj processor and (sacrifice donate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/08 14:59
-	11	mach and virtual adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/08 15:31
-	5	virtual adj processor and (hand adj off)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/08 15:31



# High Performance Computing Group

**DAC**

Departament d'Arquitectura de Computadors,  
Universitat Politècnica de Catalunya

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Overview

People **1994 HPC Group Publications**

Topics

**PhD Thesis**

Publications

Weekly  
Seminar

Related  
Projects

- Marisa Gil. *Cooperación entre la aplicación y el kernel para la planificación de flujos, en sistemas multiprocesadores, como soporte al paralelismo*. PhD thesis, Universitat Politècnica de Catalunya (UPC), July 1994. Advisor José I. Navarro.

**Journals**

- Antonio González. *Design and Evaluation of an Instruction Cache for Reducing the Cost of Branches*. *Performance Evaluation*, pp. 83-96, vol. 20, no. 1-3, May 1994.
- Dolors Royo and Miguel Valero-García. *Benchmarking: una técnica para la evaluación rápida de computadores*. *Informática y Automática*, pp. 19-27, vol. 27, no. 1, March 1994.
- Mateo Valero, Eduard Ayguadé and Montse Peiron. *Network synchronization and Out-of-Order Access to Vectors*. *Parallel Processing Letters*, pp. 405-415, vol. 4, no. 4, December 1994.
- Jordi Torres and Eduard Ayguadé. *Compilació per a Supercomputadors*. *Butlletí de les Societats Catalanes de Física, Química, Matemàtiques i Tecnologia*, pp. 235-246, vol. 14, no. 12, June 1994.

**Conference Proceedings**

- Anna M. del Corral and José M. Llaberia. *Hardware Support to Reduce Conflicts between Vector Streams*. In *2nd International Workshop on Massive Parallelism*, pp. 90-104, October 1994.
- Anna M. del Corral and José M. Llaberia. *Out-of-Order Access to Vector Elements in order to Reduce Conflicts in Vector Processors*. In *6th IEEE Symposium on Parallel and Distributed Processing (SPDP'94)*, pp. 126-134,

October 1994.

- Antonio González and Jordi Tubella. *The Multipath Parallel Execution Model for Prolog*. In *1st International Conference on Parallel Symbolic Computation (PASCO'94)*, pp. 164-173, September 1994.
- Cristina Barrado, Jesús Labarta and Eduard Ayguadé. *An Efficient Scheduling of DOACROSS Loops*. In *Sixth IASTED/ISMM International Conference Parallel and Distributed Computing and Systems*, pp. 303-307, October 1994.
- Cristina Barrado, Jesús Labarta and Patricia Borensztein. *Implementation of GTS*. In *6th International PARLE Conference (PARLE'94)*, pp. 565-576, July 1994.
- Eduard Ayguadé, Jesús Labarta, Jordi Garcia, Merce Girones and Mateo Valero. *Detecting Affinity for Automatic Data Distribution*. In *2nd International Workshop on Massive Parallelism*, pp. 32-46, October 1994.
- Eduard Ayguadé, Jordi Garcia, Merce Girones, Jesús Labarta, Jordi Torres and Mateo Valero. *Detecting and Using Affinity in an Automatic Data Distribution Tool*. In *7th International Workshop on Programming Languages and Compilers for Parallel Computing (LCPC'94)*, pp. 61-75, August 1994.
- Jordi Tubella and Antonio González. *Combining Depth-First and Breadth-First Search in Prolog Execution*. In *Joint Conference on Declarative Programming (GULP-PRODE'94)*, pp. 452-453, September 1994.
- Jordi Tubella and Antonio González. *A Partial Breadth-First Execution Model for Prolog*. In *6th IEEE International Conference on Tools with Artificial Intelligence (TAI'94)*, pp. 129-137, November 1994.
- Josep Llosa, Mateo Valero, José A.B. Fortes and Eduard Ayguadé. *Using Sacks to Organize Registers in VLIW Machines*. In *Third Joint International Conference on Vector and Parallel Processing (CONPAR'94 - VAPP VI)*, pp. 628-639, September 1994.
- Josep Llosa, Mateo Valero, Eduard Ayguadé and Jesús Labarta. *Register Requirements of Pipelined Loops and their Effect on Performance*. In *2nd International Workshop on Massive Parallelism*, pp. 173-189, October 1994.
- Juan J. Navarro, Toni Juan and Tomás Lang. *MOB Forms: A Class of Multilevel Block Algorithms for Dense Linear Algebra Operations*. In *International Conference on Supercomputing (ICS'94)*, pp. 354-363, July 1994.
- Josep-L. Larriba-Pey, Juan J. Navarro, Oriol Roig and Angel Jorba. *A generalized vision of some Parallel Bidiagonal Systems Solvers*. In *International Conference on Supercomputing (ICS'94)*, pp. 404-411, July 1994.

- Luis Díaz de Cerio, Miguel Valero-García and Antonio González. *Efficient FFT on Torus Multicomputers: A Performance Study*. In *2nd Austrian-Hungarian Workshop on Transputer Applications*, pp. 233-242, September 1994.
- Marisa Gil, Xavier Martorell and José I. Navarro. *The Enhancement of a User-level Thread Package Scheduling on Multiprocessors*. In *2nd International Conference on Software for Multiprocessors and Supercomputers, Theory, Practique and Experience*, pp. 390-398, September 1994.
- Mateo Valero, Montse Peiron and Eduard Ayguadé. *Access to Vectors in Multi-module Memories*. In *Euromicro Workshop on Parallel and Distributed Processing (PDP'94)*, pp. 228-236, January 1994.
- Mateo Valero, Montse Peiron and Eduard Ayguadé. *Memory Access Synchronization in Vector Multiprocessors*. In *Third Joint International Conference on Vector and Parallel Processing (CONPAR'94 - VAPP VI)*, pp. 414-425, September 1994.
- Montse Peiron, Mateo Valero and Eduard Ayguadé. *Synchronized Access to Streams in SIMD Vector Multiprocessor*. In *International Conference on Supercomputing (ICS'94)*, pp. 23-32, July 1994.
- Patricia Borensztein, Jesús Labarta and Cristina Barrado. *Measures of Parallelism (II)*. In *Sixth IASTED/ISMM Internacional Conference Parallel and Distributed Computing and Systems*, pp. 63-67, October 1994.

## Book Chapters

- Jordi Torres, Eduard Ayguadé, Jesús Labarta and Mateo Valero. *ALIGN and DISTRIBUTE based Linear Loop Transformations*. In *Programming Languages and Compilers for Parallel Computing*, Springer-Verlag, January 1994.

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